

a common source wiring provided on said insulating surface and connected with said at least two transistors at one of source and drain of each of said at least two transistors;

a common drain wiring provided on said insulating surface and connected with said at least two transistors at the other of the source and drain of each of said at least two transistors,

wherein said at least two transistors are connected with each other in parallel by the connections of said common gate wiring, said common source wiring and said common drain wiring with said at least two transistors, and

wherein channel-forming regions of said at least two transistors are separately provided in at least two separate islands respectively.

2. (Amended) An active matrix type display device comprising:

at least two transistors provided on an insulating surface in a decoder circuit of a driver circuit of said active matrix type display device;

a common gate wiring provided on said insulating surface and connected with said at least two transistors at gate electrodes of said at least two transistors;

a common source wiring provided on said insulating surface and connected with said at least two transistors at one of source and drain of each of said at least two transistors; and

a common drain wiring provided on said insulating surface and connected with said at least two transistors at the other of the source and drain of each of said at least two transistors,

wherein said at least two transistors are connected with each other in parallel by the connections of said common gate wiring, said common source wiring and said common drain wiring with said at least two transistors, and

wherein channel-forming regions of said at least two transistors are separately provided in at least two separate islands respectively.

3. (Amended) An active matrix type display device comprising:

at least two transistors provided on an insulating surface in a buffer circuit of a driver circuit of said active matrix type display device;

a common gate wiring provided on said insulating surface and connected with said at least two transistors at gate electrodes of said at least two transistors;

a common source wiring provided on said insulating surface and connected with said at least two transistors at one of source and drain of each of said at least two transistors; and

a common drain wiring provided on said insulating surface and connected with said at least two transistors at the other of the source and drain of each of said at least two transistors,

wherein said at least two transistors are connected with each other in parallel by the connections of said common gate wiring, said common source wiring and said common drain wiring with said at least two transistors, and

wherein channel-forming regions of said at least two transistors are separately provided in at least two separate islands respectively.

8. (Amended) An active matrix type display device comprising:

at least two transistors provided on an insulating surface in a driver circuit of said active matrix type display device;

a common gate wiring provided on said insulating surface and connected with said at least two transistors at gate electrodes of said at least two transistors;

a common source wiring provided on said insulating surface and connected with said at least two transistors at one of source and drain of each of said at least two transistors; and

a common drain wiring provided on said insulating surface and connected with said at least two transistors at the other of the source and drain of each of said at least two transistors,

wherein said at least two transistors are connected with each other in parallel by the connections of said common gate wiring, said common source wiring and said common drain wiring with said at least two transistors, and

wherein channel-forming regions of said at least two transistors are separately provided in at least two separate islands respectively.

REMARKS

The Office Action of *December 11, 2000*, has been received and its contents carefully noted. Filed concurrently herewith is a *Request for a Two (2) Month Extension of Time* that extends the shortened statutory period for response to *April 11, 2001*. Accordingly,